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Short communication

# Influence of the configuration in planar interdigitated electrochemical micro-capacitors

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### HIGHLIGHTS

- The geometric configuration plays a key-role on the performances of μ-SCs.
- ► The reduction of the interspace enhances the specific energy and power.
- ▶ A high resolution μ-SC with interspace as low as 5 μm has been realized.
- ► The RuO<sub>2</sub> based  $\mu$ -SC was able to cycle up to 100 V s<sup>-1</sup>.

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### ABSTRACT

We have investigated the influence of the geometric configuration of planar interdigitated microsupercapacitors on their specific energy and specific power. The theoretical values of the Equivalent Series Resistance (ESR) were deduced from an analytical expression of the cell constant K, and compared with experimental Electrochemical Impedance Spectroscopy (EIS) measurements performed on gold current collectors with different interdigitated patterns (varying the interspace, width, length and number of interdigitated fingers). It is shown that the configuration plays a key-role on the electrochemical performances of the device, and that the specific power is enhanced by a reduction of the interspace.

Based on these results, we have realized hydrous  $RuO_2$ -based thin films electrochemical capacitors integrated on silicon with an optimized configuration, using conventional photolithography techniques and electrodeposition from an aqueous chloride solution. The micro-supercapacitor is characterized by a cell capacitance of 0.2 mF cm<sup>-2</sup> per footprint area, and was able to cycle at a scan rate of 100 V s<sup>-1</sup>. © 2012 Elsevier B.V. All rights reserved.

### 1. Introduction

The reduction in size and power consumption of electronic circuits, sensors and actuators, has enabled the development of embedded micro-systems with greater autonomy of energy. Accordingly, harvesting micro-devices that are able to extract energy from various ambient environmental sources (thermal, mechanical, solar energy) have received an increasing research interest, in the aim of obtaining autonomous self-powered systems [1]. The source of energy is not however continuously available in the environment and an intermediate rechargeable energy storage

component as close as possible to the harvesting micro-device is essential to ensure the energy autonomy of the whole system. The harvested energy may be stored in micro-batteries (Li or Li-ions) [2], but their finite life-time may be problematic when they have to be embedded in systems with limited accessibility, such as structureembedded micro-sensors where no maintenance or replacement is possible. Micro-batteries are moreover inefficient to supply loading applications requiring high amount of power.

The high power and extended lifetime of electrochemical capacitors, miniaturized to a microscopic scale, could satisfy a variety of micro-power demands and complement or replace micro-batteries in electrical energy storage and harvesting applications when high power delivery or uptake pulses are required in very short times. The integration of low-profile supercapacitors on a chip has thus been an emerging and increasing field of research over the last decade. Two categories of supercapacitors are



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distinguished: electric double layer capacitors (EDLCs) based on carbon (activated carbon, nanostructured carbon) and pseudocapacitors based on metal oxides such as RuO<sub>2</sub>, MnO<sub>2</sub> or conductive polymers such as polypyrrole.

The first micro-supercapacitors (µ-SCs) were inspired from the technologies developed for micro-batteries, using a solid-state RuO<sub>2</sub>/ LiPON electrode/electrolyte in a stacked configuration [3–5]. Several other solid electrolytes were then experimented with the same configuration [6-8]. However, the stack configuration is limited to solid or gel-type electrolytes, which generally leads to a loss of power. Furthermore, in terms of integration, it can be very challenging as it requires numerous processing steps to pattern the different layers: current collectors, active material, electrolyte and to establish electrical connections at the end. µ-SCs based on pseudo-capacitive materials [9–13] and carbon-based electric double-layer microcapacitors [14–18] have been afterward realized on planar configuration, where electrodes of various shapes are fabricated on a same plane and electronically isolated by a physical separation. This configuration, mostly interdigitated, has indeed the advantages of easy fabrication with limited number of technological steps, suitability with all types of electrolytes (including liquid), and an easy adjustment of the patterns, which can decrease the internal impedance by minimizing the distance between adjacent electrodes [15,19].

An electrical energy storage micro-device has a limited surface area available in the electronic circuit. Following the approach taken for ion transport in 3D-microbattery [20], it is important to optimize the geometry of the current collectors for a given electrode material and to consider the performances of the  $\mu$ -SC (capacitance, energy and power) normalized to its footprint area on the chip.

In the present work, we have investigated the influence of the geometric parameters (interspace *i*, width *w*, length *L* and number of fingers N) of planar interdigitated µ-SCs on their electrochemical performances (specific energy and power per footprint area). Theoretical values were compared with experimental electrochemical impedance spectroscopy (EIS) measurements performed on different patterns. Then, the optimal configuration was used for the realization of an interdigitated  $\mu$ -SC with RuO<sub>2</sub> as the active material. Hydrous forms of ruthenium dioxide  $RuO_x \cdot nH_2O$  (denoted  $RuO_2$  thereafter) currently represent the state of the art in pseudo-capacitive materials, with high capacitance (up to 1575 F  $g^{-1}$ ), high electric conductivity (300 S  $cm^{-1}$ ) and excellent cycle life ( $>10^5$  cycles). Its high cost has limited its development for large-size supercapacitors, but the use of this material becomes relevant and promising for µ-SCs using very little amount of active material [21].

### 2. Experimental

Hydrous RuO<sub>2</sub> based µ-SCs were elaborated with different interdigitated patterns as shown in Fig. 1. The interspace *i* was varied from 5 to 100  $\mu$ m, the width *w* from 50 to 200  $\mu$ m, the length L from 100 to 1000  $\mu$ m, and the number of interdigitated fingers N from 2 to 60. The width of the component (which is the sum of the width of both pads, the interspace *i* and the length *L* of the fingers) was set to 1700 µm. The micro-devices were realized as follows: a 500 nm SiO<sub>2</sub> was first thermally grown at 1100 °C on a silicon wafer. Current collectors were then deposited using the evaporation of a 100 nm Ti/800 nm Au layer, annealed at 250 °C for 20 min, and patterned with a conventional lift-off process. The current collectors and the pads were protected by an insulating layer (40 nm Si<sub>3</sub>N<sub>4</sub>/1 µm SiO<sub>2</sub>) via a plasma enhanced chemical vapor deposition (PECVD). The interdigitated gold fingers where the active material will be subsequently deposited were then selectively opened via a second photolithography step followed by the



Fig. 1. Schematic drawing of the interdigital micro-device.

etching of the masking  $Si_3N_4/SiO_2$  layer in a buffered HF. The current collectors were finally cleaned in a sulfochromic acid solution.

Electrodeposition of hydrous ruthenium oxide onto gold current collectors was carried out from an aqueous chloride solution composed of 10 mM RuCl<sub>3</sub>·*x*H<sub>2</sub>O in  $10^{-1}$  M KCl/ $10^{-2}$  M HCl, and adjusted to pH 2 with a KOH aqueous solution. Cyclic voltammetry (CV) between -300 and +950 mV vs. Ag/AgCl at 50 mV s<sup>-1</sup>, and performed at 50 °C for 100 cycles under stirring conditions, was used to induce the growth of the RuO<sub>2</sub> deposits [22].

Electrochemical characterization of the electrodes and the micro-devices were performed in deaerated 0.5 M H<sub>2</sub>SO<sub>4</sub> electrolyte using a SP-240 potentiostat from BioLogic equipped with a linear scan generator for high scan rates. EIS measurements were carried out at open circuit potential and frequencies ranging from 1 MHz to 10 mHz. The resistivity of the deposited gold,  $\rho_{Au}$ , was 2.2  $\mu\Omega$  cm and the electrolyte (0.5 M H<sub>2</sub>SO<sub>4</sub>) resistivity,  $\rho_e$ , was 5.1  $\Omega$  cm at 19.7 °C. Scanning Electron Microscopy (SEM) was performed using a Hitachi S-4800 field-emission electron microscope.

### 3. Results and discussions

## 3.1. Theoretical determination of the ESR of planar interdigitated electrodes

An electrochemical capacitor may be represented by an ideal capacitor with an "equivalent series resistance" (ESR) increased by an "equivalent distributed resistance" (EDR) related to the distributed resistance/capacitance of the porous electrode, as defined by R. Kötz [23]. The main contributions of the ESR are the current collector resistance,  $R_{cc}$ , and the electrolyte resistance,  $R_e$ , which both depend on the configuration of the micro-cell, while the EDR depends on the properties of the active material (porosity, thickness, nanostructuration...). Therefore, the specific power will predominantly depend on the highest resistive contribution between the ESR and the EDR. In order to focus on the influence of the geometric configuration on the performances of the cell, it is hypothesized in the following that the EDR is negligible compared to the ESR. This particular case is applicable to low-EDR active materials having a fully accessible surface area such as onion-like

carbon [15], graphene [24,25] and thin films, for which the influence of the configuration of the micro-device is prominent.

The resistance of the current collectors,  $R_{cc}$ , for the different geometries was estimated from Ohm's law using a Comsol simulation, and further validated experimentally with Kelvin probes using an Agilent 4294A impedance analyzer. As it will be shown later on (see Fig. 2),  $R_{cc}$  varies only slightly with *i*, *w*, *L* and *N*.

The theoretical values of the electrolyte resistance of the microdevices,  $R_e$ , (which is given by  $R_e = K \times \rho_e$ ) have been deduced from the analytical expression of the cell constant, K, of planar interdigitated electrodes developed by W. Olthuis et al. [26], according to the following equation:

$$\mathbf{K} = \frac{1}{(N-1) \times L} \times \frac{2 \times K(k)}{K\left(\sqrt{(1-k^2)}\right)} \tag{1}$$

with K(k) the complete elliptic integral of the first kind and

$$k = \frac{i}{i+w} \quad \text{for } N = 2 \tag{2}$$

$$k = \cos\left(\frac{\pi}{2}\frac{w}{i+w}\right) \quad \text{for } N > 2 \tag{3}$$

In Fig. 2, the value of ESR, which is given by the sum of  $R_{cc}$  and  $R_e$ , has been calculated for a series of interdigitated electrodes, starting from a unique configuration set to  $i = 5 \,\mu\text{m}$ ,  $w = 100 \,\mu\text{m}$ ,  $L = 1000 \,\mu\text{m}$ 

and N = 10, and allowing one single parameter to vary in each case. This was done using previously calculated values of  $R_{cc}$  and Eq. (1) in a Matlab environment to compute the value of K (and therefore  $R_e$ ). In all cases, the electrolyte was assumed to be 0.5 M H<sub>2</sub>SO<sub>4</sub>.

As shown in Fig. 2(a), a micro-scale reduction of the interspace *i* between adjacent fingers (with  $w = 100 \mu m$ ,  $L = 1000 \mu m$  and N = 10) significantly reduces the contribution of the resistance of the electrolyte  $R_e$  and the resulting ESR. It is noteworthy that a decrease of the interspace *i* does not have any effect on the active area,  $A_a$ , of the micro-devices since  $A_a = N \times w \times L = 1 \text{ mm}^2$ . Increasing the width *w*, the length *L* or the number of fingers *N* (Fig. 2(b)–(d), respectively) lead to an increase of the active area  $A_a$ , and therefore, as expected, to a decrease of  $R_e$  and ESR.

A series of interdigitated electrodes were prepared with various values of *i*, *w*, *L* and *N* to validate the proposed model. The ESR values of the interdigitated electrodes were determined from EIS measurements performed in 0.5 M H<sub>2</sub>SO<sub>4</sub> electrolyte. The ESR values were taken at the intersection point on the real axis at high frequency. As shown in Fig. 2, all the experimentally measured ESR values (without any active material) are in close agreement with the theoretically determined values, except for the case of *i* > 40 µm, where the calculated ESR values slightly overestimate the experimentally determined values. This could be due to fringing effects [26]. In the following, we will focus on the study of interdigitated electrode with *i* = 5 µm, where the proposed model is validated by the experimental data. Also, focusing on a given electrode material and a given electrolyte, it is now important to



Fig. 2. Theoretical and Experimental evolution of Re, Rcc and ESR as a function of (a) the interspace i, (b) the width w, (c) the length L, (d) the number of interdigital fingers N.

study how the interdigitated configuration will affect the maximum specific energy and power that can be drawn from the device.

### 3.2. Influence of the configuration on the specific energy and power

As seen before, it is important to normalize the properties of  $\mu$ -SCs by the footprint aera,  $A_{fp}$ . The specific energy per footprint area,  $E_{fp}$  (in mJ cm<sup>-2</sup>), can be determined by the following equation:

$$E_{fp} = \frac{C\Delta U^2}{2 \times A_{fp}} \tag{4}$$

with  $\Delta U$  the potential range (in V).

As the absolute capacitance *C* (in F) is directly related to the active area,  $A_a$ , the maximum specific energy will be obtained by maximizing the ratio  $A_a/A_{fp}$ . This will be achieved by increasing the values of *w* and decreasing the values of *i*.

Concerning the maximum usable power per footprint area,  $P_{fp}$  (in mW cm<sup>-2</sup>), it is calculated according to the following equation:

$$P_{fp} = \frac{\Delta U^2}{4 \times R \times A_{fp}} \tag{5}$$

with R = ESR + EDR, the total effective resistance. Neglecting the EDR (see below), Eq. (5) becomes:

$$P_{fp} = \frac{\Delta U^2}{4 \times (R_{cc} + R_e) \times A_{fp}}$$
(6)

Both  $R_{cc}$  and  $A_{fp}$  will increase with an augmentation of one of the geometric parameters of the interdigitated fingers (*i*, *w*, *L* or *N*). Also, as  $R_e(i)$  is an increasing function, the best configuration for increasing  $P_{fp}(i)$  will always be the one favoring the lowest interspace *i*. Finally,  $R_e(w)$ ,  $R_e(L)$  and  $R_e(N)$  are all decreasing functions. Therefore,  $P_{fp}(w)$ ,  $P_{fp}(L)$  and  $P_{fp}(N)$  will have a maximum that will depend on both the geometry of the component and the properties of the current collectors (thickness, material).

Based on these results, we have realized interdigitated  $RuO_2$  based  $\mu$ -SCs with optimized configuration for enhanced performances.

3.3. Applications to RuO<sub>2</sub> based interdigitated microsupercapacitors

### 3.3.1. Simulation of the optimized configuration

The above procedure was applied to determine the best configuration for the realization of interdigitated RuO<sub>2</sub>-based  $\mu$ -SCs with an electrode capacitance of 1.2 mF cm<sup>-2</sup> (per active area) and negligible EDR (see below), according to the geometric specifications shown in Fig. 1 (footprint area  $A_{fp} = 1700 \times N \times (w + i)$ ).

Fig. 3 shows the theoretical evolution of the specific energy  $E_{fp}$  and specific power  $P_{fp}$  per footprint area based on Eqs. (1), (4) and (6), and using a 1 V potential range. Just like before, a single geometric parameter was varied at a time, starting from an initial configuration set to  $i = 5 \mu m$ ,  $w = 100 \mu m$ ,  $L = 1000 \mu m$  and N = 10. The specific energy and specific power are both favored for the lowest interspace *i* (Fig. 3(a)) and highest length *L* (Fig. 3(c)). Increasing the width *w* increases the specific energy  $E_{fp}$ , but at the expanse of the specific power  $P_{fp}$  (Fig. 3(b)). Regarding the number



**Fig. 3.** Theoretical evolution of the specific energy  $E_{fp}$  and specific power  $P_{fp}$  per footprint area for a RuO<sub>2</sub>  $\mu$ -SC of 1.2 mF cm<sup>-2</sup> electrode capacitance as a function of (a) the interspace *i*, (b) the width *w*, (c) the length *L*, (d) the number of interdigital fingers *N*.



Fig. 4. (a) Optical image of the interdigital fingers after RuO<sub>2</sub> electrodeposition, and (b) SEM image of RuO<sub>2</sub> deposits.



Fig. 5. (a) CVs at 100 V s<sup>-1</sup> and (b) EIS of a  $i = 5 \ \mu m$ ,  $w = 1000 \ \mu m$ ,  $L = 1000 \ \mu m$  and  $N = 6 \ RuO_2 \ \mu$ -SC.

of interdigitated fingers, Fig. 3(d) shows that  $E_{fp}$  is independent of N with a constant ratio  $(A_a/A_{fp}) = (w \times L)/(1700 \times (w + i)) = 56\%$ . However, the variation of  $P_{fp}$  shows an optimum for N = 6. This is related to the fact that  $(R_{cc} \cdot A_{fp})$  increases and  $(R_e \cdot A_{fp})$  decreases with increasing N values.

Accordingly, the following configuration was chosen as a compromise between high specific energy and high specific power:  $i = 5 \ \mu m$ ,  $L = 1000 \ \mu m$ ,  $w = 100 \ \mu m$  and N = 6.

### 3.3.2. Validation with RuO<sub>2</sub> based micro-supercapacitors

The electrodeposition of hydrous  $RuO_2$  was performed on interdigitated gold electrodes. The adherence of the deposit was good and no short-circuit was observed for the 5  $\mu$ m interspace (Fig. 4).

The RuO<sub>2</sub>  $\mu$ -SC with the  $i = 5 \mu m - L = 1000 \mu m - w = 100 \mu m - N = 6$  configuration maintains a stable capacitive behavior up to 100 V s<sup>-1</sup> (Fig. 5(a)) with a cell capacitance of 0.2 mF cm<sup>-2</sup> per footprint area ( $A_{fp} = 1.1 \text{ mm}^2$ ). The corresponding specific energy  $E_{fp}$  at 100 V s<sup>-1</sup> is 84  $\mu$ J cm<sup>-2</sup>. This result is comparable to our previous results obtained with onion-like carbon [15]. This ability to cycle at such high scan rates is due to the low value of the *RC* time constant of the interdigitated micro-supercapacitors (25  $\mu$ s), *i.e.* a low absolute capacitance *C* and a low cell resistance *R* [27].

Fig. 5(b) displays the experimental Nyquist plot of the microdevice in deaerated 0.5 M H<sub>2</sub>SO<sub>4</sub> electrolyte. No leakage current is observed, with a near vertical straight line in the low-frequency region typical of capacitive material. As expected, the value of the EDR of the RuO<sub>2</sub> thin film is very small (EDR < 1  $\Omega$ ) and negligible compared to the value of the ESR (*ca.* 14  $\Omega$ ), which is close to the predicted value (Fig. 2(d)). The corresponding specific power *P*<sub>fp</sub> for this configuration is very high, with a value of 1.7 W cm<sup>-2</sup>, in agreement with the proposed model (2.3 W cm<sup>-2</sup>, Fig. 3(d)).

### 4. Conclusions

Based on a simple model, calculations were performed and compared with experimental data to find the electrode's optimal configuration of interdigitated RuO<sub>2</sub> based micro-supercapacitors. It was shown that an appropriate geometric adjustment of the electrode patterning can enhance the specific energy and specific power of the micro-devices, especially with the reduction of the physical separation isolating the interdigitated fingers. Electrodeposition of hydrous RuO<sub>2</sub> is a promising way to obtain high resolution micro-supercapacitors with interspace as low as 5 µm, and micro-devices able to cycle at a scan rate of 100 V  $s^{-1}$ . The use of conventional UV-photolithography techniques (1 µm resolution) currently limits the size of the pattern, but further work will consider the use of the stepper projection lithography (350 nm resolution), electron beam lithography or nanoimprint lithography (sub-10 nm resolution) for further reduction of the interspace. In turn, these improvements should lead to further increase of the specific power. Also, an optimization of the electrodeposition process of the RuO<sub>2</sub> should easily allow increasing the cell capacitance without any loss in the resolution of the device [28].

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